

In the Abstract:

Please replace the original abstract with the following amended abstract.

A common electrode substrate has a transparent insulating substrate to be arranged opposite to an array substrate having pixel electrodes formed in respective pixel regions that are defined by a plurality of gate bus lines and drain bus lines. The substrate holds a liquid crystal having negative dielectric anisotropy. A common electrode is formed on the transparent insulating substrate; alignment regulating structures having linear protrusions are formed on the common electrode; and a light shield film is formed on the transparent insulating substrate with overlap regions that overlap the pixel electrodes when viewed in the direction perpendicular to a surface of the transparent insulating substrate, so as to shield, from light, alignment defective regions of the liquid crystal formed in regions of end portions of the pixel electrodes.